

In the Specification:

Please replace the paragraph beginning on page 17, line 10 with the following amended paragraph:

A driving operation according to an example 1-1 using the gate voltage regulating circuit 32 shown in Fig. 4 will be described with reference to Fig. 5. Incidentally, in this example, only one threshold value A is used as the threshold value to be inputted to the comparator 312, and it is assumed that initially, the switch 303 selects and outputs the gate-on voltage  $V_a$ . The counter 311 counts the clocks from the oscillating circuit until the synchronizing pulse of the horizontal synchronizing signal is detected (steps S1 and S3). For example, in case the horizontal scanning frequency is 50 kHz, when the count value becomes 100 ( $= 5 \text{ M}/50 \text{ k}$ ), the synchronizing pulse of the horizontal synchronizing signal is detected. When the synchronizing pulse of the horizontal synchronizing signal is detected, the comparator 312 compares the count value with the threshold value A (step S5). For example, when the threshold value A is made 77, because of the count value  $(100) >$  the threshold value A (77), the comparator 312 outputs the control signal to the switch 303 so that the gate-on voltage  $V_a$  is outputted, and the switch 303 outputs the gate-on voltage  $V_a$  (step S7). Next, the count value of the counter 311 is cleared (step S11), and until it becomes unnecessary to output the gate-on voltage  $V_g$  because of power-off or the like (step S13), the counter 311 counts the clocks

from the oscillating circuit until the synchronizing pulse of the horizontal synchronizing signal is again detected (step S1 ~~and~~ S3).